

## CLAIMS

### WHAT IS CLAIMED IS:

1. A method of synchronizing a floating point pipeline to execute floating point  
5 operations and an integer pipeline to execute integer and memory addressing operations,  
the method comprising the step of:  
stalling the floating point pipeline and the processor pipeline substantially at the  
same time when a stall condition occurs in the floating point pipeline; and  
stalling the floating point pipeline and the processor pipeline substantially at the  
10 same time when a stall condition occurs in the processor pipeline.